



**Mehdi Baboli** Ph. D.

Department of Microelectronics and Computer Engineering,  
Faculty of Electrical Engineering,  
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### **Educations:**

Ph. D. Department of Microelectronics and Computer Engineering, Faculty of Electrical Engineering University Technology of Malaysia (UTM), Johor Bahru, Malaysia, 2016. Thesis title: " Clustered Two-dimensional Mesh Topology for Large-scale Network-on-Chip Architecture ".

M. Sc. Digital and Electronics Engineering, Electrical Engineering Department, Iran University of Science and Technology, Tehran, Iran, 2007, Thesis title: "Simulation and Implementation of the Encryption Algorithm for WiMax on FPGA".

B. Sc. Electronics Engineering, Electrical Engineering Department, University of Mazandaran, Babol, Iran, 1998, Project title: " Design and Implementation scoreboard ".

### **Technical Areas of Interest:**

- Network-on-Chip
- Hardware
- Digital and Discrete System
- Computer architectures
- Robotic
- Computer networks

### **Courses Taught:**

- Microprocessor & Lab
- Computer's architecture
- Advance Computer's architecture
- Electronics
- Digital Electronics

- VLSI
- Signals and systems
- Electrical circuits & Lab
- Application software (VHDL & Verilog)
- Design digital

**Software:**

- VHDL and Verilog
- EWB
- HSPICE
- PSPICE
- LEDIT
- MATLAB
- SYSTEM C & C++

**Publication:**

1. Mehdi Baboli, Nasir Shaikh-Husin, Muhammad Nadzir Marsono, “An Empirical Evaluation of Topologies For Large Scale Noc Under Different Routing Algorithms And Buffer Size”. Proceeding of 4th international Conference in Universiti Teknologi Malaysia, Johor Bahru, Malaysia 19-21 August 2014.
2. Mehdi Baboli, Nasir Shaikh-Husin, Muhammad Nadzir Marsono and usman urrah sheikh.” A Comprehensive Evaluation of Direct and Indirect Network-on-Chip Topologies”. Proceedings of the 2014 International Conference on Industrial Engineering and Operations Management, Bali, Indonesia, January 7 - 9, 2014 , pp 2081-2090.
3. Fatemeh Asadiamiri, Mehdi Baboli, Kashif Chaudhary, Jalil Ali, and Preecha P. Yupapin. “The Effect Of Two Different Plasma Radii In Slow Waves Structure In Smooth -Wall Waveguide”. Proceeding of 4th international Conference in Universiti Teknologi Malaysia, Johor Bahru, Malaysia 19-21 August 2014.
4. Fatemeh Asadiamiri, Mehdi Baboli, Kashif Chaudhary, Jalil Ali, and Preecha P. Yupapin. “Dispension of Fast and Slow Waves for TE Mode in a Cylindrical Waveguide with Two different plasma frequency”. Proceeding of 2d International Science Postgraduate Conference 2014, (ISpC20t4) O Faculty of Science, Universiti Teknologi Malaysia .

5. Atani, R.E.; Baboli, M.; Mirzakuchaki, S.; Atani, S.E.; Zamanlooy, B."Design and implementation of a 118 MHz 2D DCT processor". Industrial Electronics, 2008. ISIE 2008. IEEE International Symposium on June 30 2008-July 2 2008 Page(s):1076 – 1081
6. Reza Ebrahimi Atani, Mehdi Baboli, Sattar Mizakuchaki. "Design and analysis of WiMAX's security system on FPGA". Mathematics And Computers in Science and Engineering archive Proceedings of the 7th WSEAS International Conference on Electronics, Hardware, Wireless and Optical Communications table of contents Cambridge, UK Pages 107-110 Year of Publication: 2008.
7. Mehdi Baboli, Fatemeh Asadi Amiri,Nasir Shaikh-Husin, and Muhammad Nadzir Marsono. " A Novel Clustering Interconnection Architecture for Large-Scale Network-on-Chip". Proceedings of the 2th International Conference in New Research on Electrical Engineering & Computer Science, Ramsar, Iran, 13 May 2016.

**Certificate:**

1. Max Digital Siemens from July 6, 1999 up to Jun 30, 1999
2. Introduction to SDH from July 24, 1999 up to Aug 4, 1999
3. MARTISDDXX Infrastructure Network phase1 from May 18, 2002 up to May 29, 2002
4. Principles of Data Communication from Jun 19, 2002 up to Jun 30, 2002
5. Basic management Briefing from Sep 19,2003 up to Nov 6, 2003
6. Protocol TCP/IP from Aug 3,2003 up to Aug 7, 2003
7. MAN( SDH-DWDMNE9) Phase1 from Dec 6, 2003 up to Dec 17, 2003
8. Statistical Analysis ( Inferential statistics), from Dec 17, 2004 up to Dec 24,2004
9. Decision Making process & Techniques (Executive) from Dec 3,2004 up to Dec 3, 2003
10. Reporting, special of Mazandaran Provides , from Apr 30, 2004 up to June 25 , 2004
11. Participatory Management , from Oct 15, 2004 up to Oct 29, 2004
12. Planning in Government Organization , from Nov 19 , 2004 up to Nov 26 , 2004
13. Promotion level , special of Mazandaran , from Feb 5 , 2004 up to Feb 26, 2004
14. Introduction to standard and Documentation concepts of work performance process ISO ,in June 18 , 2004
15. Job Relations , from Jan 21,2005 up to Jan 21 2005
16. Basic Management Level11 ,from Dec 18,2003 up to Jan 29 2004

17. Cisco Router( CCNA) , from May 13, 2006 up to May 31, 2006
18. Cisco Router ( CCNA ICND) , from Oct 28 ,2006 up to Nov 8 2006
19. Infrastructure Oct 12, 2008 up to Dec 21 , 2008
20. ZTE Router and Network's Equipments Dec 2010 (Chenzen, China)

**Experience & Skill:**

1. Mazandaran province, the official telecommunications company employee as an expert in data transferring & switching system until August 2011.
2. I am teaching at University of Science & Technology of Mazandran
3. I have good command on the Data Transferring system of Tellabs for installing and triggering and maintenance
4. I have good command on the Data Transferring system of Alcatel for installing and triggering and maintenance
5. I have good command on the routers and switches of CISCO
6. I have good command on the Transferring system SDH and PDH
7. I have good command on with computer networks and its design and implementation( Lan , Man , Wan)
8. I have good command on the WiMax system
9. I have good command on the simulators for example Modelsim ,hspice L-Edit and Xilinx
10. I have good command on Windows OS and Office programs
11. I have good command on the hardware descriptive language VHDL
12. I have good command on repayment regularities Hardware, digitals, electronics and Computers